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control.

MARKUP OF THE AMENDED PARAGRAPHS

One page, 1, line 5, the markups are as follows:

COPY OF PAPERS ORIGINALLY FILED

This application is a continuation-	in-part of [application 54208-28 filed on _	, having SN
	erence herein.] U.S. Patent Application Se	
09/191,443, filed on November 12, 1998, o	entitled A UNIVERSAL SERIAL BUS	(USB) RAM
ARCHITECTURE FOR USE WITH M	IICROCOMPUTERS VIA AN INTER	RFACE
OPTIMIZED FOR INTEGRATED SE	RVICES DEVICE NETWORK (ISDN	nd is now issued
as U.S. Patent No. 6,219,736 B1 on April	17, 2001 and is incorporated herein by refe	rence.
One page 1, line 7, the markups ar	e as follows:	
This application is related to [an a	pplication entitled "CONFIGURATION]	U.S. Patent
Application Serial Number 09/670,509, fil	ed September 26, 2000, entitled CONFIG	<u>URATION</u>
SELECTION FOR USB DEVICE [CONT	ROLLER", filed on	CONTROLLER.
One page 2, line 12, the markup		
To answer a lower-volume need	d, while still retaining a flexible interfa	ce, a different type
of USB device controller was developed	ed by the applicant. This device, a USE	3-RAM, is described
in U.S. [Application No.]Patent Application Serial Number 0	9/191,443, filed on
November 12, 1998, entitled A UNIVE	ERSAL SERIAL BUS (USB) RAM AI	RCHITECTURE
FOR USE WITH MICROCOMPUTER	RS VIA AN INTERFACE OPTIMIZE	D FOR
INTEGRATED SERVICES DEVICE	NETWORK (ISDN) and is now issued	as U.S. Patent No.
6,219,736 B1 on April 17, 2001 (5420)	8-00028), which is hereby incorporated	l by reference into
the present application. USB-RAM dev	vice controller provides a general solut	ion for connecting
to the USB by providing an interface to	the USB electrical bus and supporting	g the fixed protocol
that is associated with all USB applicat	tions. The application specific aspects	are handled by
writing packets into a common memor	y and reading packets from the memor	y under interrupt

SUPPORT FOR SERIAL NUMBERS FOR DOCKET NUMBERS SET FORTH IN SPECIFICATION